



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

BIBDATASHEET

CONFIRMATION NO. 8732

Bib Data Sheet

SERIAL NUMBER 10/773,290	FILING DATE 02/09/2004 RULE	CLASS 365	GROUP ART UNIT 2818	ATTORNEY DOCKET NO. Q79663
-----------------------------	---------------------------------------	--------------	------------------------	----------------------------------

APPLICANTS

Junichi Yamada, Tokyo, JAPAN;

** CONTINUING DATA *****

This application is a DIV of 10/211,378 08/05/2002 PAT 6,819,604 *yes LP*

** FOREIGN APPLICATIONS ***** *yes 10*

JAPAN 2001-236638 08/03/2001

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 05/06/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 24	TOTAL CLAIMS 25	INDEPENDENT CLAIMS 5
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	EXAMINER'S SIGNATURE <i>[Signature]</i>			

ADDRESS

23373
 SUGHRUE MION, PLLC
 2100 PENNSYLVANIA AVENUE, N.W.
 SUITE 800
 WASHINGTON , DC
 20037

TITLE

Semiconductor memory having a defective memory cell relieving circuit

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
RECEIVED		<input type="checkbox"/> 1.16 Fees (Filing)
		<input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
		<input type="checkbox"/> 1.18 Fees (Issue)